

FIGURE 1

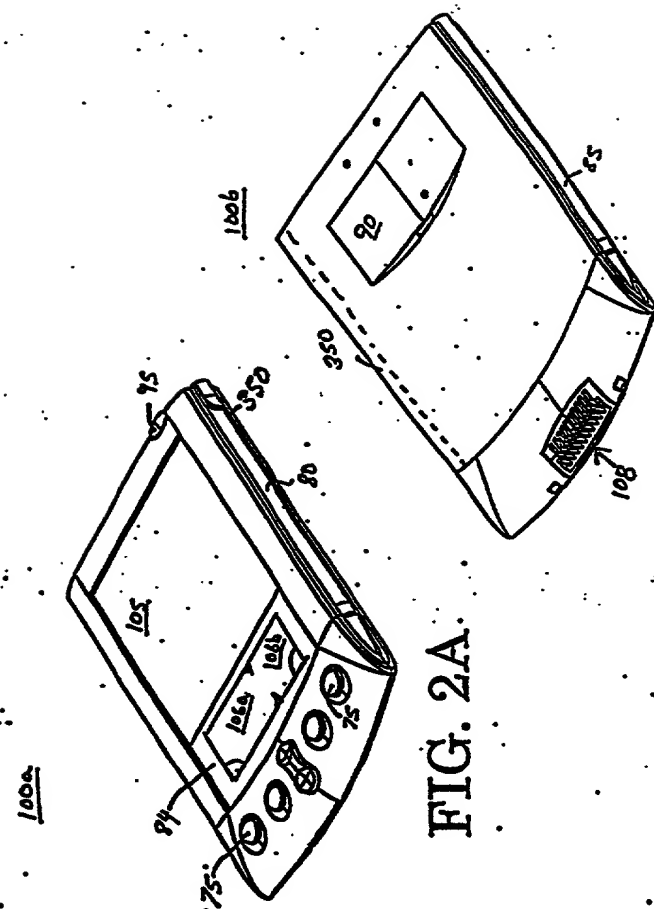
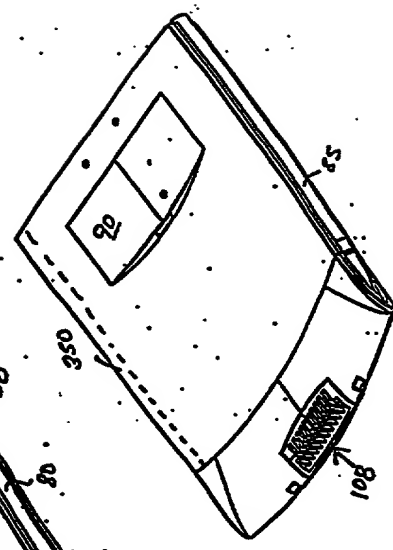


FIG. 2B



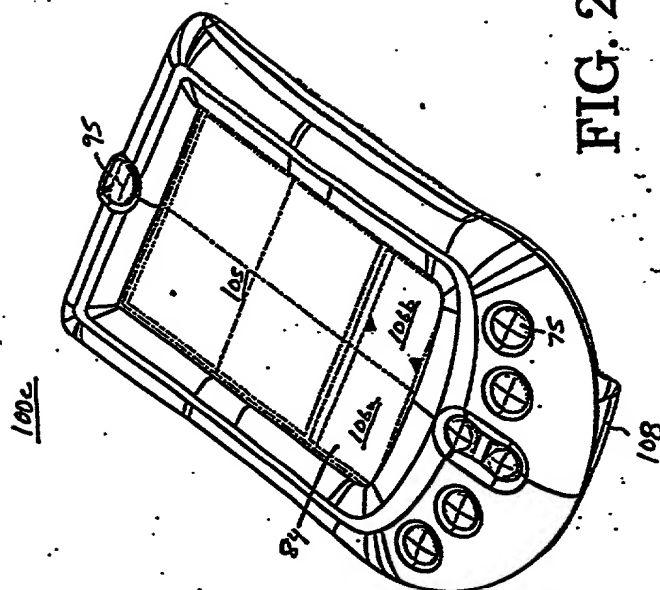


FIG. 2C

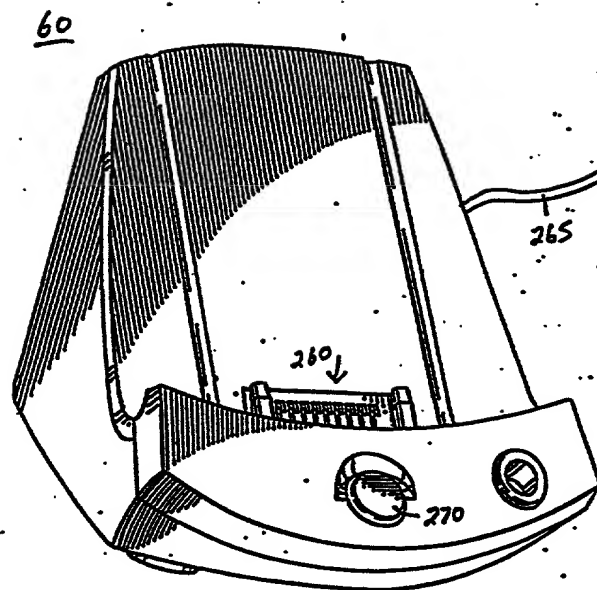


FIG. 3

FIG. 4 is a block diagram of a system 100. The system 100 includes a processor 101, a display device 105, an alpha-numeric input (digitizer) 106, an on screen cursor control 107, a signal comm serial port 108, an IR 355, a blue tooth 360, a data storage device (optional) 104, a RAM volatile 102, and a ROM non-volatile 103. The processor 101 is connected to the display device 105, the alpha-numeric input (digitizer) 106, the on screen cursor control 107, the signal comm serial port 108, the IR 355, the blue tooth 360, the data storage device (optional) 104, the RAM volatile 102, and the ROM non-volatile 103. The display device 105 is connected to the alpha-numeric input (digitizer) 106. The alpha-numeric input (digitizer) 106 is connected to the on screen cursor control 107. The on screen cursor control 107 is connected to the signal comm serial port 108. The signal comm serial port 108 is connected to the IR 355. The IR 355 is connected to the blue tooth 360. The blue tooth 360 is connected to the data storage device (optional) 104. The data storage device (optional) 104 is connected to the RAM volatile 102. The RAM volatile 102 is connected to the ROM non-volatile 103. The ROM non-volatile 103 is connected to the processor 101. The processor 101 is also connected to a wireless GSM 240.

100

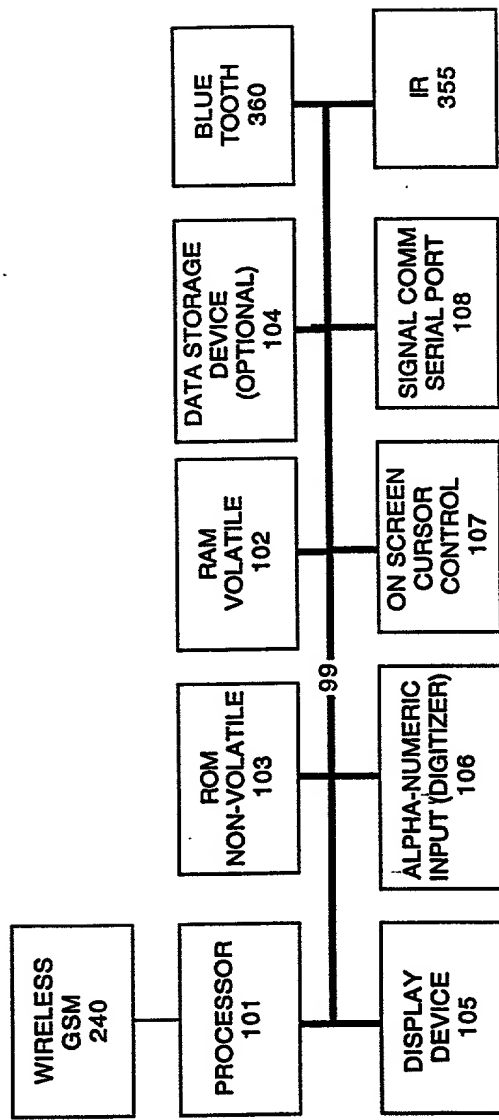


FIG. 4

440

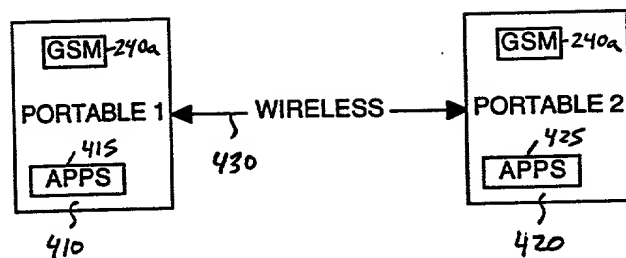


FIG. 5A

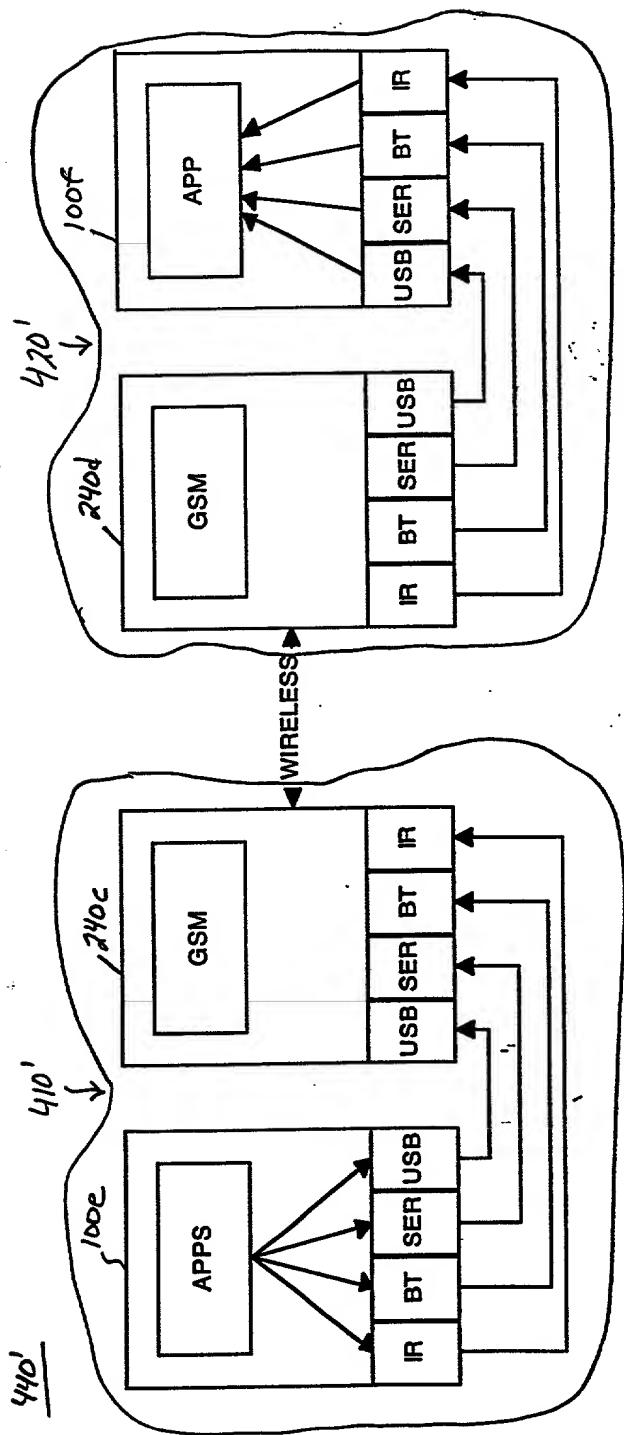


FIG. 5B

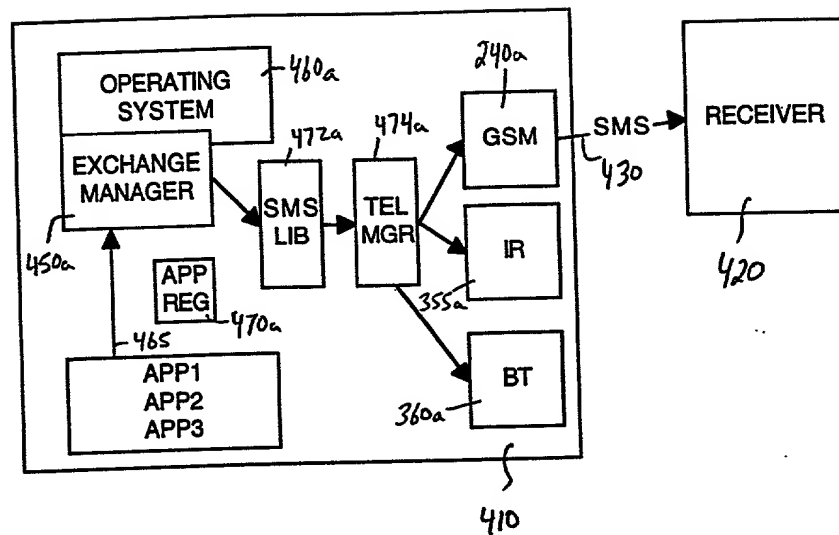


FIG. 6A



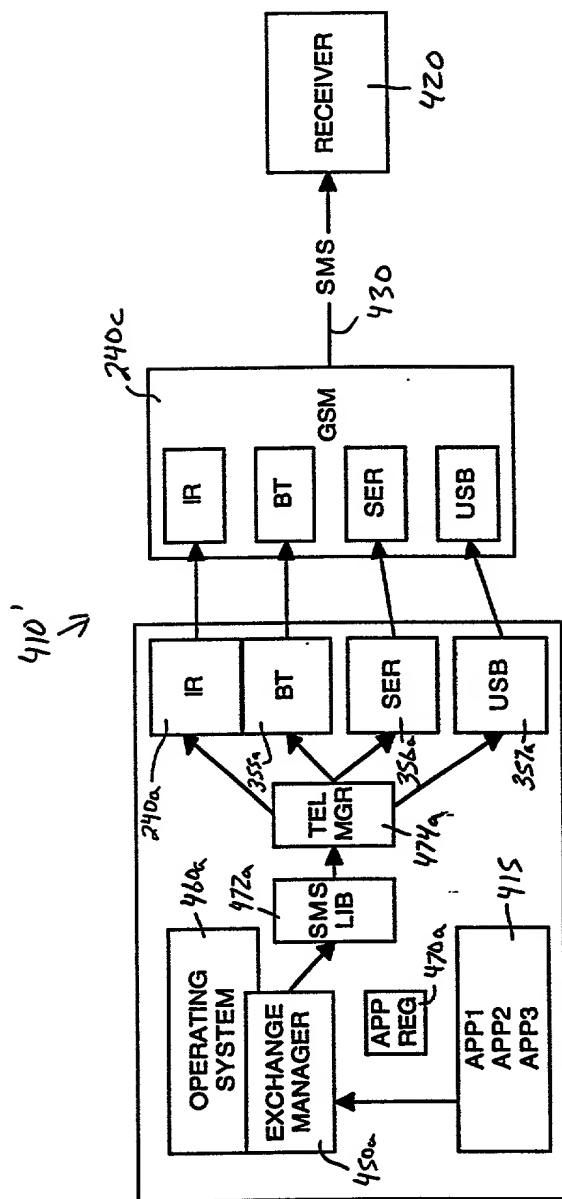


FIG. 6B

S10

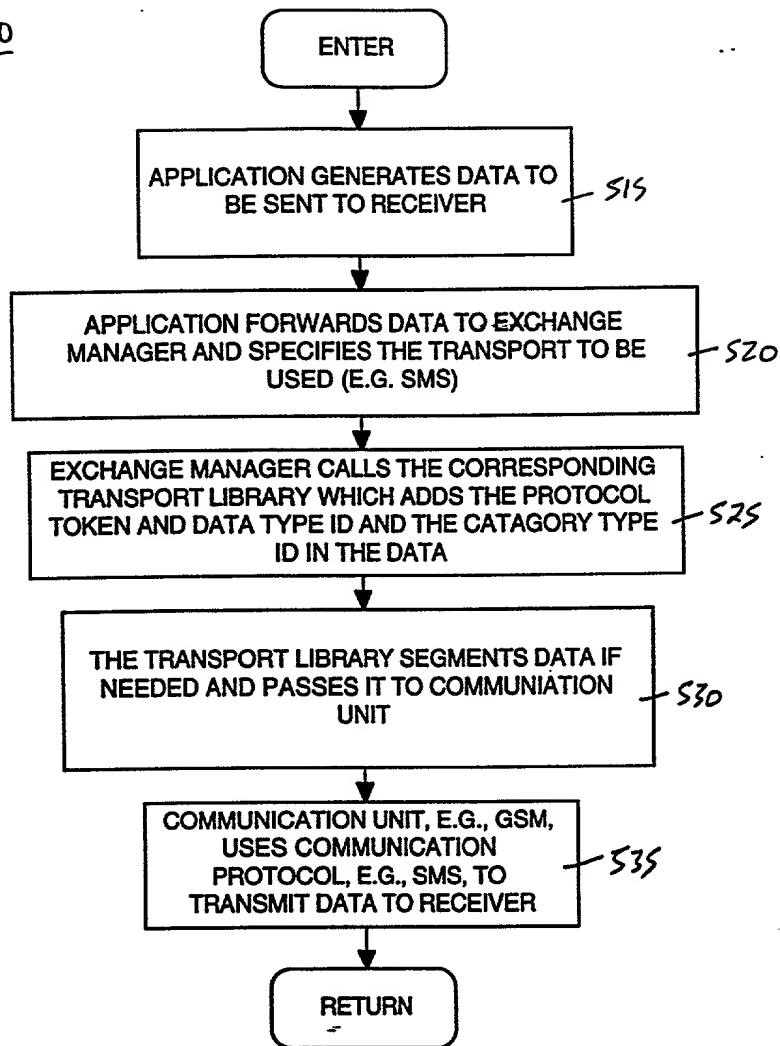


FIG. 7

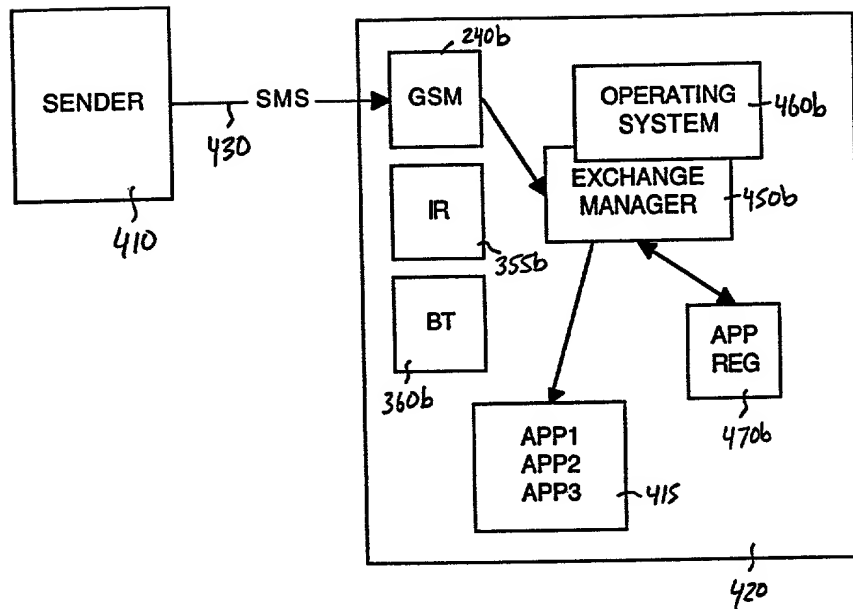


FIG. 8A

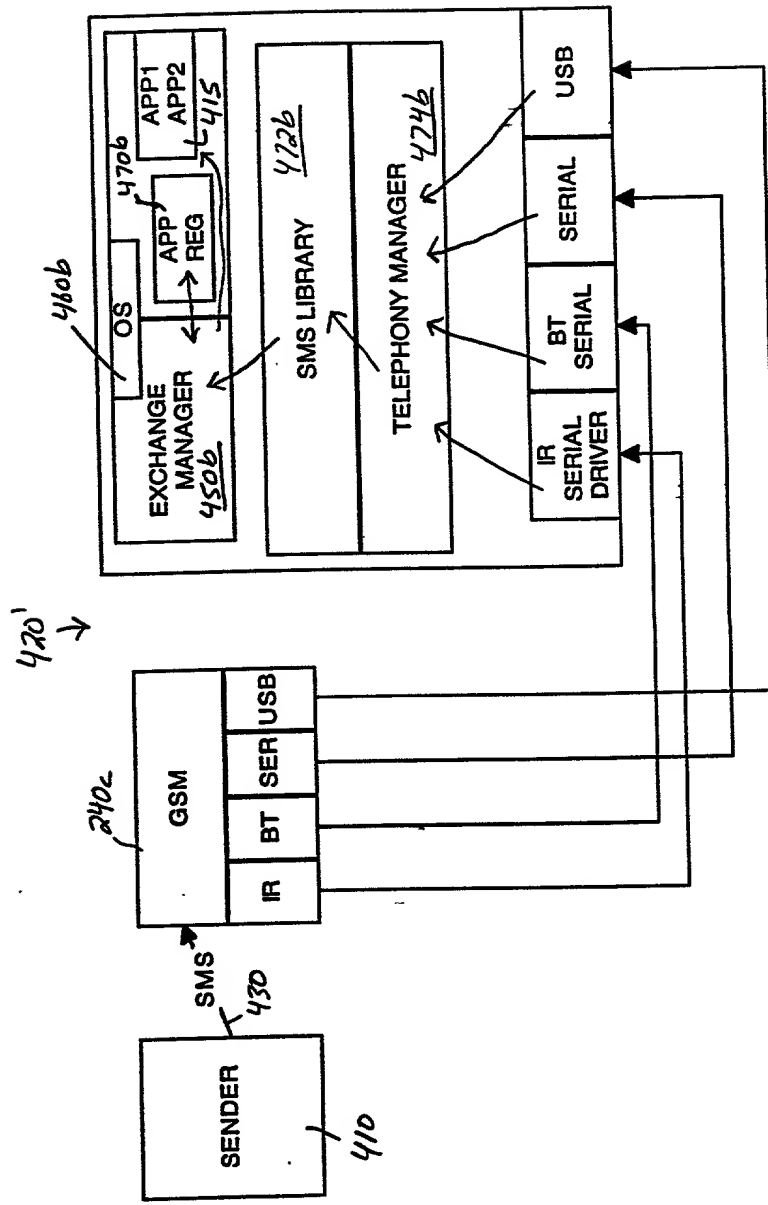


FIG. 8B

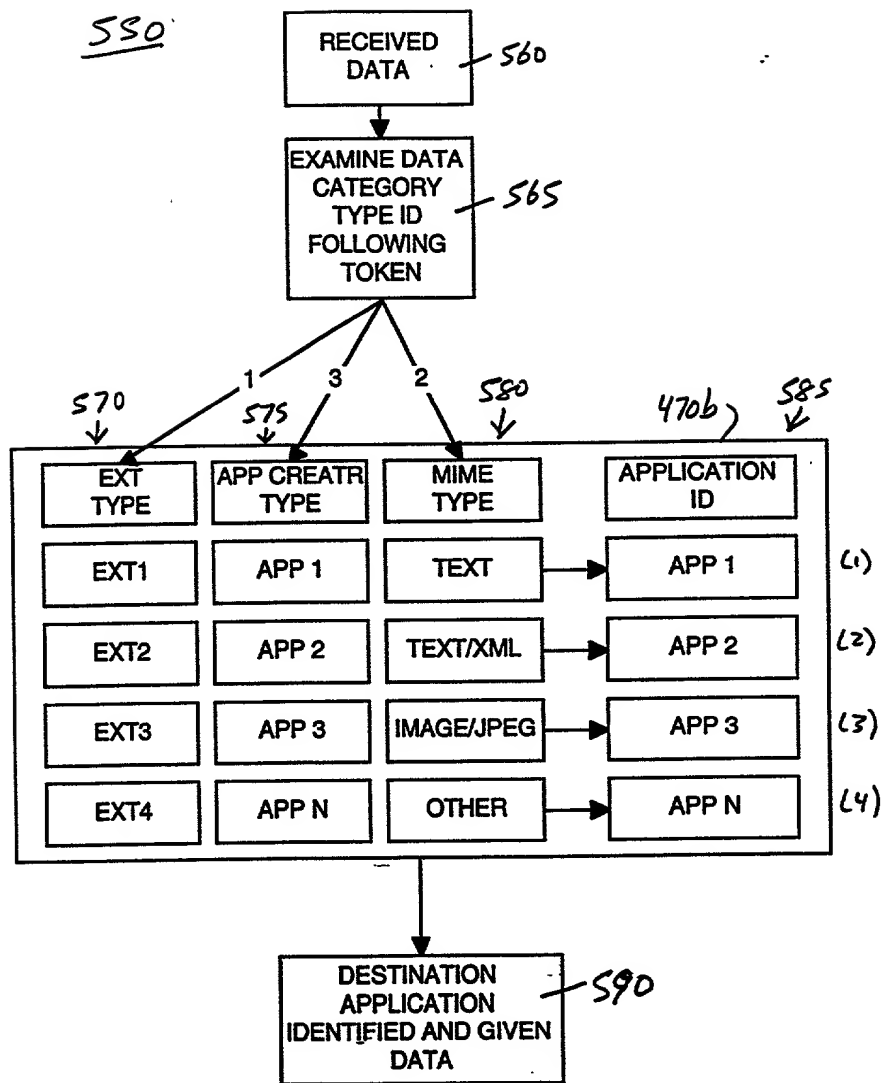


FIG. 9

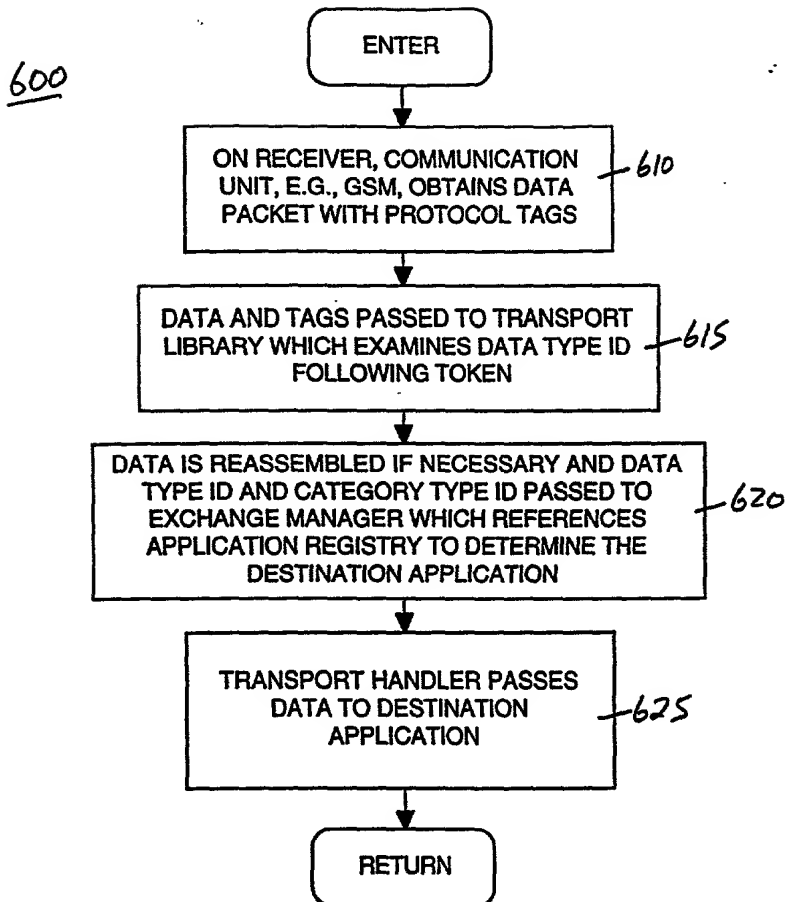


FIG. 10